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Real Time On Chip Implementation

Real Time On-Chip Implementation of Dynamical Systems with Spiking Neurons Francesco Galluppi, Sergio Davies and Steve Furber Advanced Processor Technologies Group University of Manchester, United Kingdom Terry Stewart and Chris Eliasmith Centre for Theoretical Neuroscience University of Waterloo, Ontario, Canada

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Real Time On-Chip Implementation of Dynamical Systems with Spiking Neurons . By Sergio Davies, ... We describe how to encode and decode analog values to patterns of neural spikes directly on chip. These methods take advantage of the full programmability of the ARM968 cores constituting the processing base of a SpiNNaker node, and exploit the ...

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We developed instant single-pixel imaging(ISPI), an on-chip SPI system that implements real-time imaging at a rate of 25 fps. ISPI uses the instant ghost imaging algorithm we proposed which leverages signal differences for image creation.

Instant single-pixel imaging: on-chip real-time ...

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A real-time chip implementation for adaptive video coding control Abstract: The paper presents an adaptive coding control for real-time video coding systems. Based on temporal correlations, the group-of-pictures (GOP) of a video sequence is split into one basic GOP (BGOP) and many adaptive GOPs (AGOPs) and then processed accordingly.

A real-time chip implementation for adaptive video coding ...

A Real-Time Chip Implementation for Adaptive Video Coding Control Article in IEEE Transactions on Circuits and Systems for Video Technology 14(8):1098 - 1104 · September 2004 with 18 Reads

A Real-Time Chip Implementation for Adaptive Video Coding ...

A Real-Time Clock/Calendar (RTCC) maintains accurate time in an embedded system even when the main power is turned off. Our RTCCs range from basic low-cost devices to highly integrated mid-range devices with either an I²C or SPI interface and include additional nonvolatile memory and a combination of features that are very useful when operating within a single battery-backed clock device.

Real-Time Clocks | Microchip Technology

Real-time template-based correlation was implemented on the Texas Instruments TMSC5402 Digital Signal Processing (DSP) chip to allow for the detection of abnormal QRS-complexes in electrocardiographic signals.

Real-Time ECG Analysis Using a TI TMSC54x Digital Signal ...

2 Real Time Clock (RTC) The RTC, or, sometimes referred to as time of the day, can be implemented using either a hardware or a software module. The primary function of an RTC implementation is to provide the information of time, day, week, month, and year.

AN4478, Software Real Time Clock Implementation on ...

The software Real Time Clock (RTC) is implemented using an 8-bit Timer/Counter with overflow interrupts enabled. Each timer overflow interrupt triggers an update of the software variables second, minute, hour, date, month, and year at the correct intervals. Figure 1-1. Oscillator Connection for Real Time Clock

AVR134: Real Time Clock (RTC) Using the Asynchronous Timer

Abstract: A real-time implementation of model predictive control (MPC) is presented in this paper. MPC, also known as receding horizon control and moving horizon control, is widely accepted as the controller of choice for multivariable systems that have inequality constraints on system states, inputs and outputs.

Real-time implementation of model predictive control ...

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Implementing the CHIP

Predictable Implementation of Real-Time Applications on MPSoC Petru Eles 1 Dublin, 2009
Predictable Implementation of Real-Time Applications on Multiprocessor Systems on Chip Petru Eles, Jakob Rosén, Alexandru Andrei, Zebo Peng Department of Computer and Information Science (IDA) Linköping University

Predictable Implementation of Real-Time Applications on ...

Galluppi, F, Davies, S, Furber, S, Stewart, T & Eliasmith, C 2012, Real time on-chip implementation of dynamical systems with spiking neurons. in Proceedings of the International Joint Conference on Neural Networks|Proc Int Jt Conf Neural Networks. IEEE, 2012 Annual International Joint Conference on Neural Networks, IJCNN 2012, Part of the 2012 IEEE World Congress on Computational Intelligence ...

Real time on-chip implementation of dynamical systems with ...

Computer Science; Published 2000; A 3D-DCT real-time video compression system for low complexity single-chip VLSI implementation @inproceedings{Burg2000A3R, title={A 3D-DCT real-

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time video compression system for low complexity single-chip VLSI implementation},
author={Andreas Peter Burg and Roni Keller and Jürgen Wassner and Norbert Felber and
Wolfgang Fichtner}, year={2000} }

A 3D-DCT real-time video compression system for low ...

performance [2]. Some software receivers offer real-time operations, others are used as a post-processing tool to aid in the evaluation of new techniques and algorithms. Often times, these can lend themselves to be developed into hardware, as a full-hardware implementation or a mixture of hardware and software.

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